

Amendments to the Specification

In the Abstract, please amend the following.

~~An electronic signal processing circuit contains a chain of stream processing circuits (12a-d). Linking multiplexing circuits (16a-e), link respective pairs of stream processing circuits (12a-d). Each linking multiplexing circuit (16a-e) is individually switchable to a normal mode and to a replacement mode. A linking multiplexing circuit (16a-e), when in the normal mode, provides a continuous connection for passing a first stream of samples values between the stream processing circuits (12a-d) in the respective pair. A shareable communication structure (14a-e) is coupled to the linking multiplexing circuits (16a-e). Each linking multiplexing circuit (16a-e), when in the replacement mode, provides a continuous connection for supplying successive sample values from a second stream from the communication structure (14a-e) to a receiving one of the stream processing circuits (12a-d) in the respective pair of the linking multiplexing circuit (16a-e). A control circuit (18) keeps a selectable one of the multiplexing circuits (16a-e) in the replacement mode so that the selectable one of the linking multiplexing circuits (16a-e) passes a stream of successive sample from the second stream to the receiving one of the processing circuits in the respective pair of linking multiplexing circuit (16a-e), while keeping at least part of the other linking multiplexing circuits (16a-e) in the normal mode.~~

Consistent with an example embodiment, there is a signal processing circuit. Linking multiplexing (L-MUX) circuits, link respective pairs of stream processing circuits. Each L-MUX circuit is individually switchable to a normal mode and a replacement mode; in normal mode, it passes a first stream of samples values between the stream processing circuits in the respective pair of the L-MUX circuits; in replacement mode, it supplies successive sample values from a second stream from the communication structure to a receiving one of the stream processing circuits in the respective pair. A control circuit keeps a selectable one of the multiplexing circuits in the replacement mode so the selectable one of the L-MUX circuits passes a stream of successive samples from the second stream to the receiving one of the processing circuits in the respective pair of L-MUX circuits, while keeping at least part of the other L-MUX circuits in the normal mode.